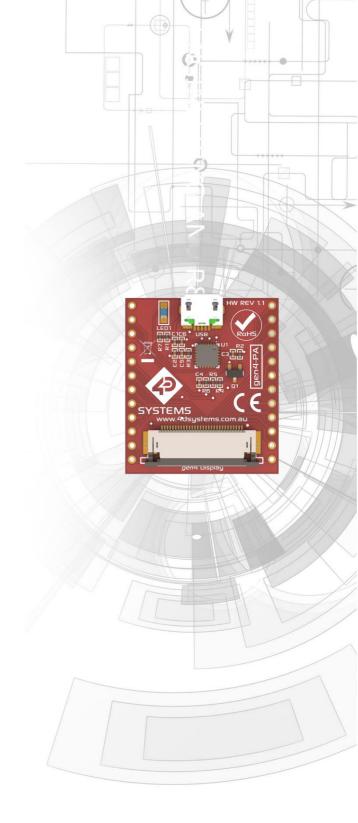


gen4-Programming Adaptor

# **DATASHEET**

DOCUMENT DATE: 05th MARCH 2019

DOCUMENT REVISION: 1.5



gen4-PA

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Please refer to the 4D Systems website for the latest
Revision of this document

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#### 1. Description

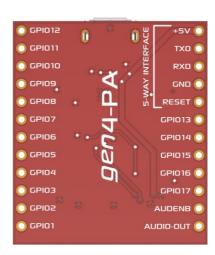
This datasheet covers the gen4-PA (Programming Adaptor) which is compatible with all of the gen4 range of Intelligent Display modules which feature Picaso or Diablo16 graphics processors. It is included in the Starter Kit (SK) packs, and is a quick and easy way to interface and program the gen4 range, without having to break out the signals from the 30-way FFC cable.

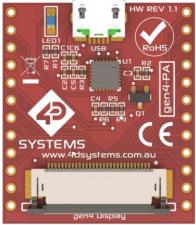
The gen4-PA is a programming adaptor for the gen4 display module that also breaks out the signals found on the 30-way FFC cable coming from your gen4 display module. These signals are available via 2.54mm pitch (0.1") pads around the edge of the gen4-PA. It also groups the common 5-way signals found on other 4D Systems products (and the gen4-IB), for easy interface to other products such as our -AR and -PI kits for the Arduino and Raspberry Pi.

The gen4-PA replaces the need for a separate standard programmer, such as the uUSB-PA5 or 4D Programming Cable. The gen4-PA has the circuitry found on the uUSB-PA5-II built into it, and features a microUSB jack, for connection to your PC. Cable is not included with the gen4-PA or in our Starter Kits.

The gen4-PA can be used for programming gen4 display modules, interfacing to a breadboard for prototyping, interfacing to Arduino and Raspberry Pi interfaces (see gen4 -AR and -PI kits), or for interfacing to virtually any host.

The pinout naming, shown on the back of the gen4-PA has been generalised to GPIOx naming, as the gen4-PA can be used with both Picaso and Diablo16 gen4 products, and the naming of GPIO is different for these. Please refer to the following table.





GEN4-PA	DIABLO16	PICASO
GPIO1	PA3	IO1
GPIO2	PA2	IO2
GPIO3	PA1	IO3
GPIO4	PA0	IO4
GPIO5	PA9	BUS5
GPIO6	PA8	BUS4
GPIO7	PA7	BUS3
GPIO8	PA6	BUS2
GPIO9	PA5	BUS1
GPIO10	PA4	BUS0
GPIO11	PA10	BUS6
GPIO12	PA11	BUS7
GPIO13	PA12	IO5
GPIO14	PA13	RX1
GPIO15	PA14	TX1
GPIO16	PA15	I2C_SCL
GPIO17	N/C	I2C_SDA

The gen4-PA utilises the Silicon Labs CP2104 USB to Serial Bridge IC. More information about this can be found from the Silicon Labs website. A link to the driver is available on our website.

- USB 2.0 compliant Full Speed 12Mbps maximum speed.
- Hardware or Xon/Xoff handshaking supported, 300bps to 2Mbps
- UART supports 5, 6, 7, 8 data bits, 1, 1.5, 2 stop bits, odd/even/mark/space and no parity
- Supports Windows 2000 and above, MAC (OSX-8 and above) and Linux (2.4 kernel and above)
- USB powered
- -40 to +85 degrees Celsius temp range

### 2. Example Hardware Connections

The following pictures illustrate how to connect the gen4-PA to various hardware. Note, the display module illustrated below is the gen4-uLCD-32DT.

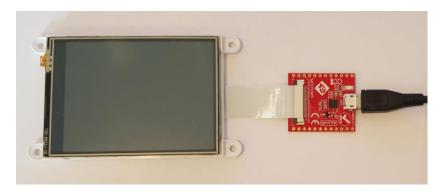


Figure 1. Typical connection of gen4 display module to gen4-PA with USB cable (not included)

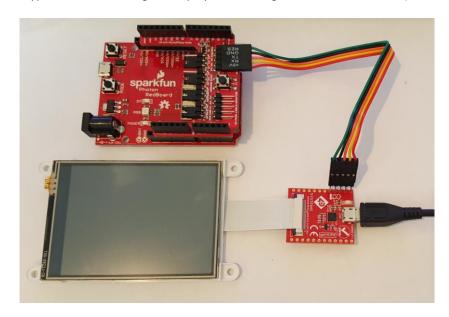


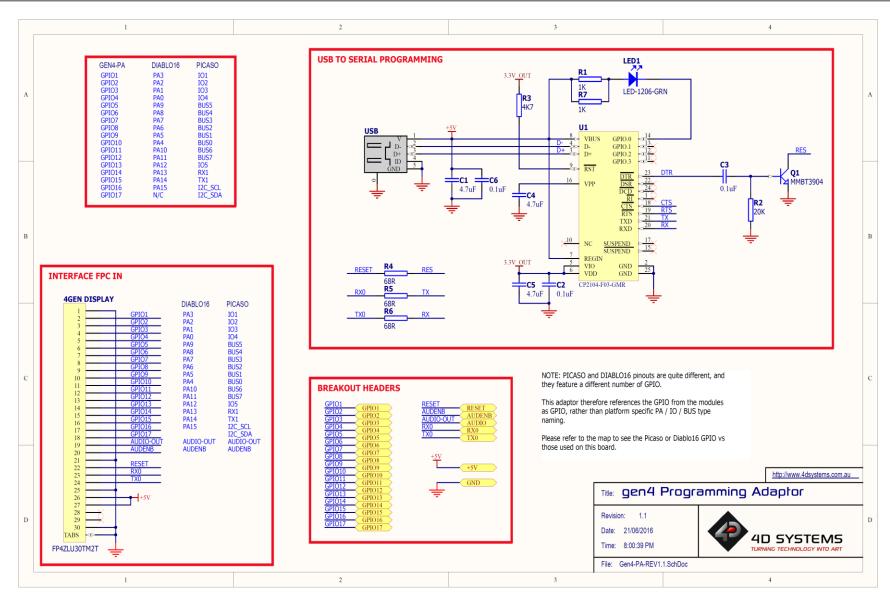
Figure 2. gen4 display, connected to the gen4-PA, and 5-way cable connecting to the Arduino Adaptor Shield.

When connecting another device (such as an Arduino) to the 5-way interface pins on the gen4-PA, this is utilising UARTO on the gen4 display which is also used by the USB controller to program the gen4 display module. Each time you program to the display module, the 5-way cable needs to be disconnected so the serial UART will not have conflicts and fail. Alternatively, separately wiring to other GPIO pins on the gen4-PA to utilise the UART1/2/3 (as is available on selected gen4 display modules) will allow this conflict to be avoided, due to utilising a separate UART. Adjustments to the settings in Workshop4 to utilise comms to a different UART is required.

The Standard FFC cable supplied has the following specifications:

30 Pin Flexible Flat Cable, 150mm Long, 0.5mm (0.02") pitch Cable Type: AWM 20624 80C 60V VW-1 Heat Resistance 80 Degrees Celsius Connections on the opposite side at each end (Type B)

# 3. Schematic Diagram



## 4. Hardware Revision History

Revision Number	Date	Description
1.1	21/06/2016	Initial Public Release Version

# 5. Datasheet Revision History

Revision Number	Date	Description
1.1	21/06/2016	Initial Draft
1.2	07/03/2019	Cosmetic Changes to gen4-PA Datasheet

## 6. Legal Notice

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